



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

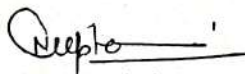
Diploma in Electronics and Instrumentation Engineering

Session 2018-2021


IV- SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	DTMA301	Applied Mathematics-III		3	1	0	60	20	20	0	0	100	4
2	ML301	Environment and Energy Studies		4	0	0	60	20	20	0	0	100	4
3	DTEI401	Instrumentation and Process Control		2	1	2	60	20	20	30	20	150	4
4	DTEI402	Microprocessors 8085 and Interfacing		2	1	2	60	20	20	30	20	150	4
5	DTEI403	Electronic Devices and Circuits		2	1	2	60	20	20	30	20	150	4
6	DTEI404	Electronics Workshop-I		0	0	2	0	0	0	30	20	50	1
7	DTEI405	Software Lab		0	0	2	0	0	0	30	20	50	1
TOTAL				13	4	10	300	100	100	150	100	750	22

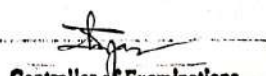
*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore


Registrar

Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore


Controller of Examinations

Shri Vaishnav Vidyapeeth Vishwavidyalaya